

## DIGITAL TRANSFORMATION TECHNOLOGY FOR A DIGITAL TRAMSFORMATION AGE



Specification		Description
Device	PIC18F45K22	SMT Device, non-removable, bootloader capable
	Oscillator	Ceramic oscillator, 8 MHZ
PSU	3v3   5v0	Dual power supply, selectable by jumper
	DC	DC Jack, 2.1 MM
	Polarity Protection	1N4007 diode, provides reverse polarity protection
Programming	ICSP	Standard ICSP Header, 5 PIN for microchip devices
	Bootloader	Firmware included enabling the device to be reprogrammed over a serial connection.
1/0	PORTS A, B, C, D, E	All I/O tracked to headers that enable easy access to the devices I/O
Communication	ICSP Header	Programming interface
	USB UART	MCP2200 enabling USB to UART communication with the PIC. The device is USB powered capable.
Extras	Power Led, Reset switch	

## DEVCC 40

The DEVCC 40 is a development platform for the PIC18F45K22. The K Type, 18 F device has a number of extensible features which enable the user to develop a feature rich system.

The development PCB is configured so that there is compatibility with other members in the family of development PCBs, without sacrificing PIN accessibility. There is a standard PIN out across the range. This standard configuration means that the code developed on the DEV-V40 can be migrated to the following

- DEVCC V18 [BlueBerry]
- DEVCC V20 [BlueBerry]
- DEVCC V28 [BlueBerry]

The standard layout supports an interchangeable shield as an accessory, which clips into the PCB.

